

S/N 09/653,618

PATENT

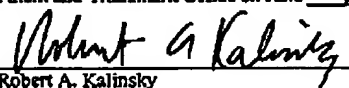
## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Paya et al.	Examiner:	Arezo Sherkat
Serial No.:	09/653,618	Group Art Unit:	2131
Filed:	August 31, 2000	Docket No.:	MS #147265.1/40062.67US01
Customer No.:	27488	Confirmation No.:	4621
Title:	Input Buffer Overrun Checking and Prevention		

CERTIFICATE UNDER 37 CFR 1.6(d)

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on June 24, 2005.

By:



Name: Robert A. Kalinsky

AMENDMENT AND RESPONSERECEIVED  
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JUN 24 2005

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the final Office Action mailed January 11, 2005 and the Advisory Action mailed March 8, 2005, please consider and enter the following amendments and remarks.

Claims 1, 2, 5-8, 11-14, and 17-20 remain pending in the application.

Amendments to the claims begin at page 2 of this paper.

Remarks begin at page 6 of this paper.